

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,187,732 B2
APPLICATION NO. : 10/629429
DATED : March 6, 2007
INVENTOR(S) : Alpaslan Demir

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

At column 2, line 35, after the word "upper", delete "bound" and insert therefor --boundary--.

At column 2, Table I heading, line 1, before the word "Allowed", delete "Integration" and insert therefor --Integrations--.

At column 3, line 61, after the word "preferably", delete "1/12" and insert therefor --1/2--.

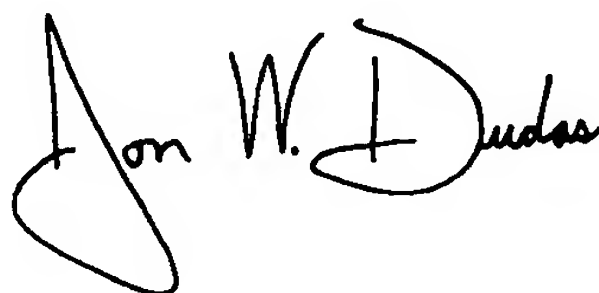
IN THE CLAIMS

At claim 2, column 5, line 21, after the word "voltage", delete "control", and insert therefor --controlled--.

At claim 2, column 7, line 10, after "(ISC)", delete "Processing" and insert therefor --processing--.

Signed and Sealed this

Eighteenth Day of September, 2007

A handwritten signature in black ink, reading "Jon W. Dudas". The signature is stylized, with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS
Director of the United States Patent and Trademark Office